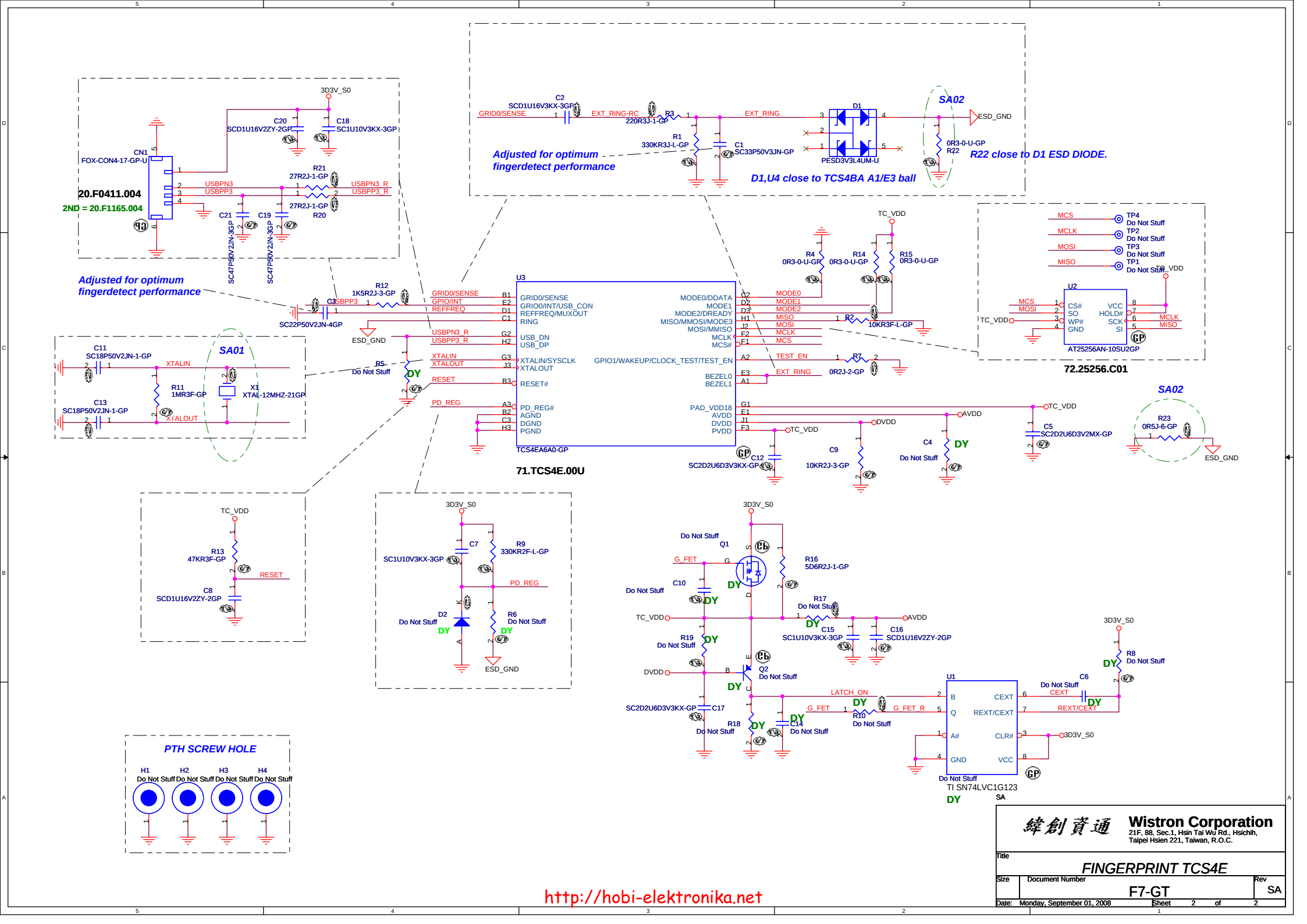
20.F0411.004
2ND = 20.F1165.004
CN1
FOX-CON4-17-GP-U
3D3V_S0
C20
SCD1U16V2ZY-2GP
C18
SC1U10V3KX-3GP
R21
27R2J-1-GP
R20
27R2J-1-GP
C21
C19
SC47P50V2JN-3GP
USBPN3
USBPP3
USBPN3_R
USBPP3_R
Adjusted for optimum fingerdetect performance
C2
SCD1U16V3KX-3GP
GRID0/SENSE
EXT_RING-RC
R3
220R3J-1-GP
EXT_RING
D1
PESD3V3L4UM-U
R1
330KR3J-L-GP
C1
SC33P50V3JN-GP
SA02
ESD_GND
0R3-0-U-GP
R22
R22 close to D1 ESD DIODE.
D1,U4 close to TCS4BA A1/E3 ball
C3
SC22P50V2JN-4GP
R12
1K5R2J-3-GP
U3
GRID0/SENSE
GPIO/INT
REFFREQ
RING
USB_DN
USB_DP
XTALIN/SYSCLK
XTALOUT
RESET#
PD_REG#
AGND
DGND
PGND
TCS4EA6A0-GP
71.TCS4E.00U
MODE0/DDATA
MODE1
MODE2/DREADY
MISO/MMOSI/MODE3
MOSI/MMISO
MCLK
MCS#
GPIO1/WAKEUP/CLOCK_TEST/TEST_EN
BEZEL0
BEZEL1
PAD_VDD18
AVDD
DVDD
PVDD
TC_VDD
R4
0R3-0-U-GP
R14
0R3-0-U-GP
R15
0R3-0-U-GP
R2
10KR3F-L-GP
R7
0R2J-2-GP
TEST_EN
EXT_RING
C12
SC2D2U6D3V3KX-GP
C9
10KR2J-3-GP
C4
Do Not Stuff
C5
SC2D2U6D3V2MX-GP
R23
0R5J-6-GP
SA02
MCS
MCLK
MOSI
MISO
TP4
TP2
TP3
TP1
Do Not Stuff
U2
CS#
SO
WP#
GND
VCC
HOLD#
SCK
SI
AT25256AN-10SU2GP
72.25256.C01
C11
SC18P50V2JN-1-GP
C13
SC18P50V2JN-1-GP
XTALIN
XTALOUT
R11
1MR3F-GP
X1
XTAL-12MHZ-21GP
SA01
R5
Do Not Stuff
R13
47KR3F-GP
C8
SCD1U16V2ZY-2GP
RESET
C7
SC1U10V3KX-3GP
R9
330KR2F-L-GP
PD_REG
D2
Do Not Stuff
R6
Do Not Stuff
Q1
Do Not Stuff
G_FET
C10
Do Not Stuff
R16
5D6R2J-1-GP
R17
Do Not Stuff
C15
SC1U10V3KX-3GP
C16
SCD1U16V2ZY-2GP
R19
Do Not Stuff
Q2
Do Not Stuff
C17
SC2D2U6D3V3KX-GP
R18
Do Not Stuff
C14
Do Not Stuff
LATCH_ON
G_FET_R
R10
Do Not Stuff
U1
B
Q
A#
GND
CEXT
REXT/CEXT
CLR#
VCC
TI SN74LVC1G123
C6
Do Not Stuff
R8
Do Not Stuff
PTH SCREW HOLE
H1
H2
H3
H4
Do Not Stuff
DY
SA
http://hobi-elektronika.net
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